

PATENT NUMBER

U.S. UTILITY Patent Application

<p><b>O.I.P.E.</b></p> <p>SCANNED <i>SD</i> Q.A. <i>SS</i></p>	<p><b>PATENT DATE</b></p>
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APPLICATION NO. 09/872796	CONT/PRIOR D	CLASS 438 029	SUBCLASS 025.010	ART UNIT 3812 3813	EXAMINER A. J. ...
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## APPLICANTS

## TITLE

# Architecture for high throughput semiconductor processing applications

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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